**What is CanAssist?**
CanAssist is an organization at the University of Victoria that is dedicated to developing and delivering innovative technologies, programs, and services that improve the quality of life of people with disabilities. For more information on CanAssist, please visit www.canassist.ca

**Position Description:**
CanAssist has an opening for an Electronics Design Technician Co-op to perform assistive technology development for individuals of all ages and across the disability spectrum.

**Responsibilities:**
Specific tasks will include background research of existing technologies, conceptualization and design of new technology, fabrication and testing of prototypes plus development and delivery of finished products. The successful candidate will be responsible for carrying out mixed analog and digital circuit design and layout, embedded firmware development, PCBA manufacturing, assembly, trouble shooting and repair as well as maintaining, cleaning and stocking an electronics lab space.

**Qualifications:**
This position is suitable for an eligible Co-op student enrolled in an electrical engineering program who has a creative mind with a desire to think outside the box beyond typical classroom and text book environments. Prototype fabrication skills are a key competency for this position.

The successful candidate must have completed their second year of engineering and have completed at least two work terms. Candidates having documented experience using engineering design tools such as Altium Designer or equivalent will be at an advantage. Experience working in a shop environment or electronics lab is a very strong asset. The successful candidate will have well-developed problem solving skills, the ability to communicate effectively with team members and be able to work with clients.
Preferred Additional Skills and Qualifications:

- Experience working in a diverse environment
- Experience working with the disability community, including the seniors’ population
- Experience working in an electronics lab or similar environment Demonstrated hands on design and fabrication experience(for example; Capstone projects, hobbies and other work related experience)
- Demonstrated experience with embedded firmware development and PCB design and fabrication

Submission Requirements and Contact Information:
Please submit, in PDF format, a cover letter, resume and contact information for three references to Paul Green, Engineering Manager (greenp@uvic.ca), by Thursday, March 1, 2018 at 4:30 pm. Late applications will not be considered. Only those candidates selected for interviews will be contacted.